

# Zikai Liu

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## Education

2023–Present	<b>ETH Zurich, PhD Student in Computer Science</b> <ul style="list-style-type: none"><li>Supervised by Prof. Timothy Roscoe. Systems Group. Anticipated graduation date: 11/2027.</li></ul>
2021–2023	<b>ETH Zurich, MSc in Computer Science</b> <ul style="list-style-type: none"><li>Major in Data Management Systems. GPA 5.67/6.00.</li><li>Thesis: Generating Trustworthy I<sup>2</sup>C Stacks Across Software and Hardware.</li></ul>
2017–2021	<b>University of Illinois at Urbana-Champaign, BSc in Computer Engineering</b> <b>Zhejiang University, BEng in Electronics and Computer Engineering</b> <ul style="list-style-type: none"><li>Dual Bachelor's degrees. GPA 3.93/4.00. Highest Honor.</li><li>Thesis: Using Concolic Execution to Provide Automatic Feedback on LC-3 Programs.</li></ul>
Focus Areas	Operating Systems, Computer architecture, Compiler, Software Testing, Cloud Computing.

## Experience

2018–2021	<b>ZJU-UIUC Robotics Team, Control/Vision Group Lead and Project Manager</b> <ul style="list-style-type: none"><li>Led the development of embedded control programs and a vision system for robots.</li><li>Managed the development process for 30+ team members as the Project Manager.</li></ul>
Summer 2020	<b>NetEase Games, Platform Engineer Intern</b> <ul style="list-style-type: none"><li>Developed a driver module with GUI to manage various joysticks through a unified interface, providing plug-and-play user experience on the NetEase android emulator.</li></ul>

## Projects

Present	<b>The Enzian Project</b> <ul style="list-style-type: none"><li>Enzian: a research computer built at the ETH Systems Group with a cache coherence interconnect (similar to CXL) between a server-class CPU and an FPGA.</li><li>Investigating accelerating applications by exploiting coherence protocols at the message level.</li></ul>
Present	<b>The Trustworthy Board Management Controller (BMC) Project</b> <ul style="list-style-type: none"><li>BMCs: exist in every modern computer to manage critical low-level functionalities.</li><li>Worked on securing BMCs by adopting a formally verified OS kernel and generating hybrid software/hardware drivers using model checking, code generation, and high-level synthesis.</li></ul>
Present	<b>The Sockeye Project</b> <ul style="list-style-type: none"><li>The project: formally models increasingly complicated hardware to build better OS.</li><li>Worked on integrating seL4 microkernel as one of the CPU drivers.</li></ul>

Fall 2022	<b>In-Hand 3D Scanning System on a Mixed Reality Headset</b> <ul style="list-style-type: none"> <li>Developed a system for near-real-time 3D scanning and reconstruction of irregular geometries, using the depth camera on Microsoft HoloLens 2.</li> </ul>
2018–2021	<b>KLC3 Symbolic Execution Engine</b> <ul style="list-style-type: none"> <li>A symbolic execution engine for LC-3 (an educational assembly) based on KLEE for automatic bug detection and test case generation, written in C/C++.</li> <li>Used to provide automatic end-to-end feedback to 100+ sophomore students for their LC-3 assignments in Fall 2020. Got uniformly positive survey responses.</li> </ul>
Spring 2021	<b>Wireless Charging Desk with Vision-Assisted Automatic Alignment</b> <ul style="list-style-type: none"> <li>Designed and implemented a desk that automatically aligns wireless charging coils with devices using a mechanical system and computer vision.</li> <li>Senior design team project. We got the Most Interdisciplinary Project Award.</li> </ul>
Fall 2020	<b>Pipelined RISC-V Processor Design Project</b> <ul style="list-style-type: none"> <li>Designed and simulated a 5-stage pipelined RV32I processor with parameterized caches, tournament branch predictions, and a prefetcher, written in SystemVerilog.</li> </ul> <p>More on my website <a href="https://zikailiu.com/projects">🌐 zikailiu.com/projects</a></p>

## Publications

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*Equal contributors	<p>Mainframe-Style Channel Controllers for Modern Disaggregated Memory Systems. <b>Zikai Liu</b>, Jasmin Schult, Pengcheng Xu, Timothy Roscoe. <i>16th ACM SIGOPS Asia-Pacific Workshop on Systems (APSys'25)</i>, October 2025 (to appear).</p> <p>Efeu: generating efficient, verified, hybrid hardware/software drivers for I2C devices. Daniel Schwyn*, <b>Zikai Liu*</b>, Timothy Roscoe. <i>20th European Conference on Computer Systems (EuroSys'25)</i>, March–April 2025.</p> <p>Verified fault handling for modern board management controllers. Ben Fiedler, <b>Zikai Liu</b>, David Cock, Timothy Roscoe. <i>20th Formal Aspects of Component Software (FACS'24)</i>, September 2024.</p> <p>End-to-End Automation of Feedback on Student Assembly Programs. <b>Zikai Liu</b>, Tingkai Liu, Qi Li, Wenqing Luo, Steven S. Lumetta. <i>36th ACM/IEEE International Conference on Automated Software Engineering (ASE'21)</i>, November 2021.</p>
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## Skills

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Programming	C/C++, Python, SystemVerilog/VHDL, OCaml, C#, JavaScript, HTML/CSS, SQL, Rust, assembly.
Operating Systems	Unix/Linux commands, shell scripts, kernel development; Windows drivers.
Tools and Frameworks	Build systems (e.g. CMake), version controls (e.g. Git), compiler frameworks (e.g. LLVM), FPGA toolchains (e.g. Vivado), debuggers and profilers (e.g. GDB), GUI frameworks (e.g. Qt).
Miscellaneous	Adobe Photoshop/Illustrator/Lightroom, photography, Unity engine.